



emPC-A500

ARM Freescale i.MX515
Based Embedded
Controller



FEATURES

Processor

- Freescale i.MX515 application processor with 600 MHz
- Fanless cooling concept
- 32 kB instruction and data caches
- Unified 256 kB L2 Cache
- Vector floating point co-processor

Memory

- System memory 256 MB DDR2 RAM
- Boot Flash 2 MB, with boot loader
- Battery-free NVRAM 32 kB
- Internal CompactFlash Socket, Type I/II

Clocks and Timers

- Secure Real Time clock (coin cell battery needed)
- Watchdog timer

Video

- OpenGL® ES2.0 and OpenVGTM 1.1 hardware accelerator
- 24 bit display support up to WXGA resolution
- 15-pin VGA graphic interface on front panel
- single channel LVDS on internal connector

Expansion

- iX-Bus 8/16 bit interface for I/O and low speed peripherals

Front Panel Connectors

- 2 x 10/100 MBit/s Ethernet (RJ45 connectors)
- 2 x USB (v2.0), with 500 mA power supply capability
- 2 x 9-pin D-SUB connectors for CAN or serial ports

Reset

- Reset push button at front panel

CAN

- SJA 1000 CAN Controller
- Up to 2 CAN channels available onboard
- Physical Layer ISO/DIS 11898-2, isolated from logic
- Bus termination is software switchable, 120 Ω resistor

Power Supply

- Input 9...34 V_{DC}
- Power consumption ca. 6 W

Physical

- Ambient operating temperature -40°C ... +70°C
- Non-operating temperature range from -40°C ... 75°C
- Humidity 0 % ~ 80 %, non-condensing
- Dimensions (w x h x d): 111 x 49 x 104 mm
- Weight approx. 0.8 kg

Options

- CODESYS IEC61131-3 runtime environment
- integrated I/O modules (see external data sheet)

Supported Operating Systems

- Windows CE 6.0
- Linux
- other operating systems on request

For special images contact your regional sales office.