

# Seminar

# Design and Validation of Multi-Gigabit Systems

## *Challenges of Embedded Design and High-Speed Interconnect*

- Analysis of Signal Integrity on High-Speed Interfaces
- Design and Evaluation of Multi-Gigabit Transmission Lines
- Power Integrity and Power Supply Analysis
- PCI Express
- Live demonstrations:  
Eye diagrams, TDR, Oscilloscope vs. VNA, Impedance Plots
- Q&A



**Date:** 22nd Oct 2019  
**Time:** 9am - 4:30pm  
**Place:** DESY campus,  
Building 28c,  
2nd floor, seminar room FLASH  
**Registration:** <https://indico.desy.de/indico/event/24167/>